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S-17-02

PATENT  
(5580-04402/BP2055.2)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/067,038  
Filed: February 4, 2002  
Inventor(s):  
Tse-Yu Yeh

§ Examiner: Unknown  
§ Group/Art Unit: 2183  
§ Atty. Dkt. No: 5580-04402  
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Title: POWER CONSUMPTION  
REDUCTION  
MECHANISM FOR  
PIPELINE STALLS

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Office of Initial Patent Examination, Customer Service Center, Washington, DC 20231, on the date indicated below.

Lawrence J. Merkel

Name of Registered Representative

Signature

May 6, 2002

Date

INFORMATION DISCLOSURE STATEMENT

RECEIVED

Commissioner for Patents  
Washington, D.C. 20231

MAY 10 2002

Technology Center 2100

Sir:

Applicant requests consideration of ☒ the references listed on the attached Form PTO-1449 and/or ☒ the additional information identified below in paragraph 3. A copy of each reference listed on the Form PTO-1449 is enclosed.

1. This Information Disclosure Statement is submitted:

- a. ☐ within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);  
☐ within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;  
☒ before the mailing date of a first Office Action on the merits; or  
☐ before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.
- b. ☐ after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: ☐ the certification of paragraph 2 below is provided, or ☐ a fee of \$180.00 is enclosed.

- c. ☐ after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.

2. It is hereby certified:

- ☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement, or
- ☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three months prior to the filing of the Statement.

3. ☒ Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:

U.S. Patent Application Serial No. 10/066,941 (5580-04401)  
U.S. Patent Application Serial No. 10/066,984 (5580-04403)  
p. 1 entitled "Background of the Invention" from the specification of the captioned application.

4. For each non-English language reference listed on the attached Form PTO-1449:

- ☐ reference is made to an English language translation submitted herewith, and/or
- ☐ reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or
- ☐ reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
- ☐ reference is made to the concise explanation contained in the specification of the present application at page(s) \_\_\_\_\_, and/or
- ☐ reference is made to the concise explanation set forth below:

5. ☐ Applicant also offers the following comments for the Examiner's consideration:

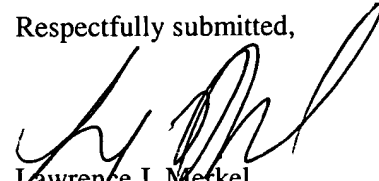
6. ☐ Also enclosed is a copy of a foreign search report citing these references.

7. ☐ The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.

8. ☐ Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

If any required fees are missing, the Commissioner is authorized to charge said fees to Conley, Rose & Tayon, P.C. Deposit Account No. 50-1505/5580-04402/LJM.

Respectfully submitted,



Lawrence J. Merkel  
Reg. No. 41,191  
Agent for Applicant(s)

CONLEY, ROSE & TAYON, P.C.  
P. O. Box 398  
Austin, Texas 78767  
(512) 476-1400

Date: May 6, 2002

**Form PTO-1449** (modified)

List of Patents and Publications

For Applicant's Information

Disclosure Statement

(Use several sheets if necessary)

ATTY. DKT. NO. 5580-04402

APPLICANT: Yeh

FILING DATE: February 4, 2002

SERIAL NO. 10/067,038

GROUP: 2183

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A1	5,630,157	5/13/97	Dwyer, III			
	A2	6,195,744	2/27/01	Favor, et al.			
	A3	5,887,185	3/23/99	Lynch			
	A4	5,673,426	9/30/97	Shen, et al.			
	A5	5,933,618	8/3/99	Tran, et al.			
	A6	5,991,884	11/23/99	Lin, et al.			
	A7	6,282,663	8/28/01	Khazam			
	A8	6,219,796	4/17/01	Bartley			
	A9	5,666,506	9/9/97	Hesson, et al.			
	A10	5,655,096	8/5/97	Branigin			
	A11	5,625,837	4/29/97	Popescu, et al.			
	A12	5,546,593	8/13/96	Kimura, et al.			
	A13	5,497,499	3/5/96	Garg, et al.			
	A14	5,488,730	1/30/96	Brown, III, et al.			
	A15	5,487,156	1/23/96	Popescu, et al.			
	A16	5,790,827	8/4/98	Leung			
	A17	5,784,588	7/21/98	Leung			

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

A18	Patterson and Hennessy, "Computer Architecture: A Quantitative Approach," Morgan Kaufmann Publishers, Inc., 1990, pages 291-299.
A19	Patterson and Hennessy, "Computer Architecture: A Quantitative Approach," Second Edition, Morgan Kaufmann Publishers, Inc., 1990, 1996, pages 242-251.
A20	SiByte, "Target Applications," <a href="http://sibyte.com/mercurian/applications.htm">http://sibyte.com/mercurian/applications.htm</a> , January 15, 2001, 2 pages.
A21	SiByte, "SiByte Technology," <a href="http://sibyte.com/mercurian/technology.htm">http://sibyte.com/mercurian/technology.htm</a> , January 15, 2001, 3 pages.
A22	SiByte, "The Mercurian Processor," <a href="http://sibyte.com/mercurian">http://sibyte.com/mercurian</a> , January 15, 2001, 2 pages.
A23	SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.
A24	SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)

**Form PTO-1449** (modified)  
 List of Patents and Publications  
 For Applicant's Information  
 Disclosure Statement  
 (Use several sheets if necessary)

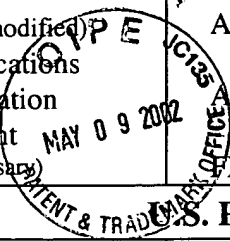
ATTY. DKT. NO. 5580-04402

APPLICANT: Yeh

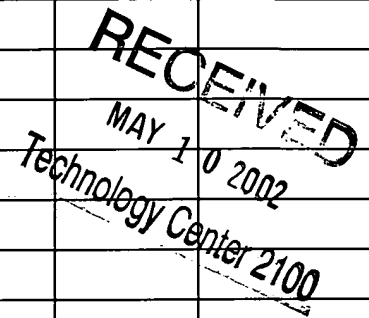
FILING DATE: February 4, 2002

SERIAL NO. 10/067,038

GROUP: 2183

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

	A25	†Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.
	A26	□ Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.
	A27	†Tom R. Halfhill, "SiByte Reveals 64-Bit Core For NPUs; Independent MIPS64 Design Combines Low Power, High Performance," Microdesign Resources, June 2000, Microprocessor Report, 4 pages.
	A28	†SiByte, Letter from Anu Sundaresan, May 18, 2000, 1 page.

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.